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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Nam-Yul YU et al. Art Unit: 2133

Examiner: Stephen M. Baker Serial No.: 10/622,508

Filed: July 21, 2003

For: APPARTAUS AND METHOD FOR

REDUCING BIT ERROR RATES (BER) AND FRAME ERROR RATES (FER) USING TURBO DECODING IN A

DIGITAL COMMUNICATION SYSTEM

SUBMISSION OF SUBSTITUTE DECLARATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants submit herewith a Substitute Declaration in connection with the above-identified application.

Although no fees or charges are believed due, the Commissioner is hereby authorized to charge any fees which may become due in connection with this communication or credit any overpayment to Deposit Account No. 18-2220.

Respectfully submitted,

Raymond B. Persino

Reg. No. 58,082

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Dated: February 7, 2006